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Response Under 37 CFR § 1.116  
Expedited Procedure - Group 2183

PATENT APPLICATION

03500.017602.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
: Examiner: David J. Huisman  
ATSUSHI DATE )  
: Group Art Unit: 2183  
Application No.: 10/671,785 )  
: Filed: September 29, 2003 )  
: For: PROCESSOR SYSTEM WITH )  
: BUILT-IN PROCESSOR, )  
: MEMORY CONTROLLER AND )  
: EXTERNAL BUS INTERFACE : August 24, 2007

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL REJECTION  
AND STATEMENT OF SUBSTANCE OF INTERVIEW

Sir:

In response to the final Office Action dated May 24, 2007, please amend the  
above-identified application, as follows:

I hereby certify that this correspondence is being transmitted via facsimile  
to the U.S. Patent and Trademark Office at (571) 273-8300, on:

August 24, 2007  
(Date of Transmission)

John D. Magliyen, Reg. No. 56,867  
(Name of Attorney for Applicant)

  
Signature

August 24, 2007  
Date of Signature